



09/382,929

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Paul A. Farrar	Examiner:	David Graybill
Serial No.:	09/382,929	Group Art Unit:	2827
Filed:	August 25, 1999	Docket:	303.603US1
Title:	<u>PACKAGING OF ELECTRONIC CHIPS WITH AIR-BRIDGE STRUCTURES</u>		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Pursuant to 37 C.F.R. §1.97(c)(2), please charge the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p) to Deposit Account No. 19-0743.

04/25/2005 SDENB0B1 00000006 190743 09382929

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The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

PAUL A. FARRAR

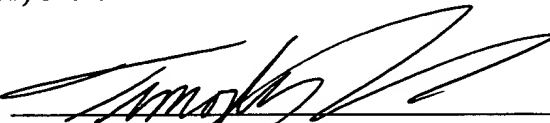
By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
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(612) 349-9587

Date

20 April '05

By


Timothy B Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 20 day of April, 2005.

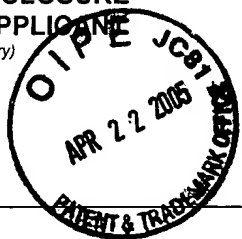
JONATHAN FERGUSON
Name


Signature

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/382,929
Filing Date	August 25, 1999
First Named Inventor	Farrar, Paul
Group Art Unit	2827
Examiner Name	Graybill, David

Sheet 1 of 1

Attorney Docket No: 303.603US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2005/0026351-A1	02/03/2005	Farrar, P. A.	09/01/2004
	US-2005/0029663-A1	02/10/2005	Farrar, P. A.	08/30/2004
	US-5,303,555	04/19/1994	Chrysler, Gregory M., et al.	10/29/1992
	US-5,461,003	10/24/1995	Havemann, R. H., et al.	05/27/1994
	US-5,488,015	01/30/1996	Havemann, Robert H., et al.	05/20/1994
	US-5,691,565	11/25/1997	Manning, M.	09/24/1996
	US-5,773,363	06/30/1998	Derderian, G. J., et al.	06/13/1996
	US-6,025,015	02/15/2000	Landry-Coltrain, C. J., et al.	12/29/1997
	US-6,156,374	12/05/2000	Forbes, L. , et al.	03/16/1999
	US-6,251,470	06/26/2001	Forbes, Leonard , et al.	10/09/1997
	US-6,413,827	07/02/2002	Farrar, Paul A.	02/14/2000
	US-6,501,179	12/31/2002	Juengling, W. , et al.	08/02/2001
	US-6,614,092	09/02/2003	Eldridge, Jerome M., et al.	08/10/2001
	US-6,667,219	12/23/2003	Eldridge, J. M.	08/30/2000
	US-6,670,719	12/20/2003	Eldridge, Jerome M., et al.	06/27/2001
	US-6,674,167	01/06/2004	Ahn, Kie Y., et al.	05/31/2000
	US-6,679,315	01/20/2004	Cosley, M. R., et al.	01/14/2002
	US-6,686,654	02/03/2004	Farrar, Paul A., et al.	08/31/2001
	US-6,709,968	03/23/2004	Eldridge, Jerome M., et al.	08/16/2000
	US-6,747,347	06/08/2004	Farrar, Paul A., et al.	08/30/2001
	US-6,838,764	01/04/2005	Farrar, P. A.	06/24/2002
	US-6,872,671	03/29/2005	Farrar, P. A.	06/24/2002

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		BLODGETT, A J., et al., "Thermal Conduction Module: A High-Performance Multilayer Ceramic Package", IBM Journal of Research and Development, 26(1), (1982),30-36	
		SINGER, PETER , "The New Low-K Candidate: It's a Gas", Semiconductor International, 22(3), (March 1999),38	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached